

## **IN THE SPECIFICATION**

Please replace the paragraphs beginning at page 2 line 19 with the following rewritten paragraphs:

-- U.S. Patent No. 6,448,848 to Altmann discloses a differential transconductor-capacitor (gm-C) circuit employing two transistors to calibrate or adjust the output signals. The control signals are coming from the voltages of a detecting circuit. However, in the detecting circuit, the common mode reference voltage is not compared directly with the input signals, instead it is compared with the intermediate signals ~~out and outB~~, such that the output signals "out and outB" may not be accurately controlled.

When the two output signals "out and outB" may not be completely symmetric to each other, due to the manufacturing procedures of the semiconductor process, the voltage difference may not be suitably improved by the detecting circuit. The detecting circuit may only be used to improve the average voltage of the two output signals, such that the two output signals "out and outB" may have different DC voltage levels, and such that the signal distortions may occur for the large signals.--.